



TOPSTAR TECHNOLOGY INDUSTRIAL CO., LIMITED

产品规格书

Product Specification Sheet

TOP-XFP-CWDM-ZR-XX

RoHS Compliant 10Gb/s XFP CWDM 80km Optical Transceiver



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PRODUCT FEATURES

- Hot pluggable
- Support 9.95Gb/s to 11.1Gb/s bit rates
- Below < 1.6W power dissipation
- XFPMS A package with duplex LC connector
- Digital Diagnostic Monitor Interface
- Very low EMI and excellent ESD protection
- Cooled CWDM EML laser and APD Receiver
- Up to 80KM for single mode fiber
- operating temperature range 0°C to 70°C
- No reference clock requirement

APPLICATIONS

- 10GBASE-ZR/ZW Ethernet
- SONET OC-192/SDH STM-64
- Other optical links

STANDARD

- XFPMSA Compliant
- SFF-8472 revision 9.5 compliant
- IEEE 802.3-2005 compliant
- Telcordia GR-468-CORE compliant
- FCC 47 CFR Part 15, Class B compliant
- FDA 21 CFR 1040.10 and 1040.11, class 1 compliant
- RoHS compliant



PRODUCT DESCRIPTIONS

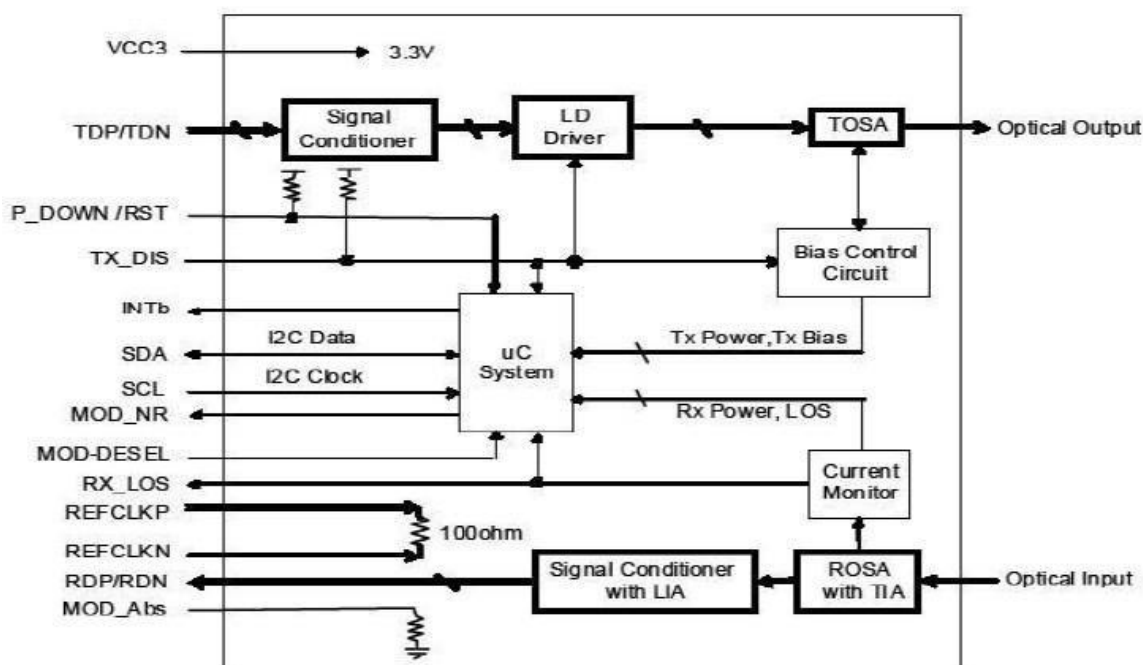
XFP 10G 80KM CWDM transceivers are de-signed for 10G Ethernet10GBASE-ZR/ZW per

802.3ae and10GSOIOC-192/SDHSTM-64,and it can support data-rate from9.953Gb/s to

11.1Gb/s. Digital diagnostics are available via I2C interface as specified in the XFPMSA.

The transceiver designs are optimized for high per-formance and cost effective to supply customers the best solutions for data-comand telecom applications.

FUNCTIONAL DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Mi	Ma	Unit	Note
SupplyVoltage	Vc	-0.5	4.0	V	
StorageTemperature		-40	85	°C	
RelativeHumidity			85	%	

Note:Stress in excess of the maximum absolute ratings can cause permanent damage to the module



GENERAL OPERATING CHARACTERISTICS

Paramet	Symbol	Min.	Typ	Max.	Unit	Note
DataRate	Ethernet		10.3125		Gb/s	
	FiberChannel		9.953			
SupplyVoltage	Vc	3.14	3.	3.46	V	
	Vc				V	
SupplyCurrent	Icc				m	
	Icc			460	m	
OperatingCaseTemp.	T	0		70	°	

ELECTRICAL INPUT/OUTPUT CHARACTERISTICS

Transmitter

Parame	Symbol	Min.	Typ	Max.	Unit	Note
Diff.inputvoltageswing		120		820	mVpp	1
TxDisableinput	H	VIH	2.0	Vcc+0.3	V	
	L	VIL	0	0.8		
TxFaultoutput	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.8		
InputDiff.Impedance	Zin		100		Ω	

Receiver

Paramet	Symbol	Min.	Typ	Max.	Unit	Note
Diff.outputvoltageswing		340	650	800	mVpp	3
RxLOOutput	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.		

Note1)TD+/-are internally AC coupled with100Ω differential termination inside the module.

Note2)TxFault and RxLOS are open collector outputs, which should be pulled up with4.7k to10kΩ resistors on the host board. Pull up voltage between2.0VandVcc+0.3V.

Note3)RD+/- outputs are internally AC coupled, and should be terminated with100Ω(differential)at the user SERDES.

OPTICAL CHARACTERISTICS

Transmitter(0~70 @10.3125Gb/s)

Parame	Symbol	Min.	Typ	Max.	Unit	Note
OperatingWavelength	λ	1470	x	1610	n	
Ave.outputpower(Enabled)	P	0		4	dBm	1
ExtinctionRatio	E	9			dB	1
RMS spectralwidth	Δ			1	nm	
Rise/Falltime(20%~80%)	Tr/Tf			50	ps	2



Optical modulation amplitude	OMA	-2.1			dBm	
Dispersion penalty				2	dB	
Output Optical Eye	IEEE802.3-2005 Compliant					

Notes:

1. "λ" is: 1470, 1490, 1510, 1530, 1550, 1570, 1590, 1610, please see the "product selection".

Receiver (0~70 @10.3125Gb/s)

Paramet	Symbol	Min.	Typ	Max.	Unit	Note
Operating Wavelength		1200		1610	n	
Sensitivity	P _{sen}			-23	dBm	3
Min. overload	P _{imax}	-7			dBm	
LOS Assert	P	-30			dBm	
LOS De-assert	P			-25	dBm	
LOS Hysteresis	P _d -P _a	0.5		4	dB	

Note 1) Measured at 10.3125Gb/s with PRBS231-1NRZ test pattern.

Note 2) 20%~80%

Note 3) Under the ER worst case, measured at 10.3125Gb/s with PRBS231-1NRZ test pattern for BER <

1x10⁻¹²

SERIAL INTERFACE FOR ID AND DDM

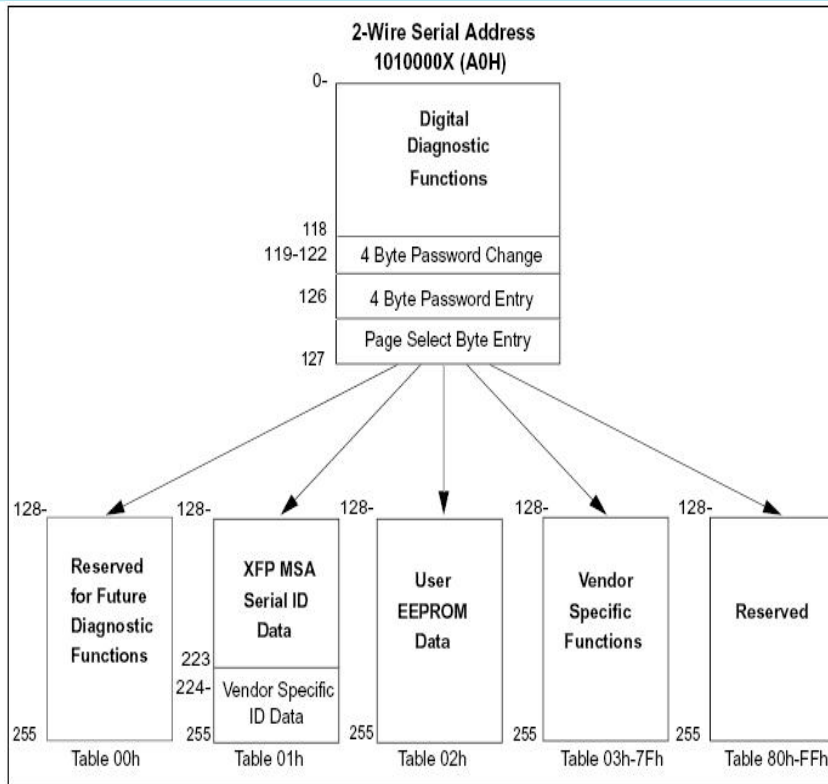
The XFP modules implement the 2-wire serial communication protocol as defined in the XFP MSA.

The serial ID information of the XFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information (A0h) is listed in Table 2. And the DDM specification (A2h) is described in Table 3. For more details of the memory map and byte definitions, please refer to the

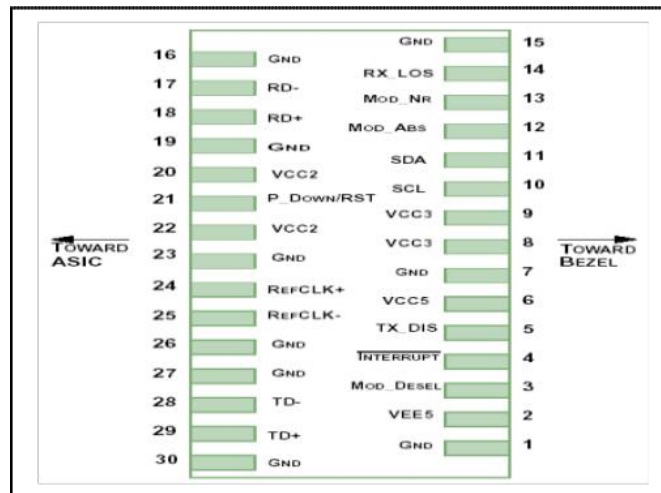
SFF-8472 (Rev 9.3, Aug. 2002), "Digital Diagnostic Monitoring Interface for Optical Transceivers".

The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)



PIN DEFINITIONS AND FUNCTIONS



PIN#	Nam	Function	Name/Descri	Notes
1		GN D	ModuleGround	1
2		VEE5	Optional-5.2VPowerSupply (Not required)	



3	LVTTTL-I	MOD_DE	Module De-select; When held low allow the module to respond to 2-wire serial interface	
4	LVTTTL-O	INTb	Interrupt; Indicates presence of an important condition which can be read via the 2-wire serial interface	2
5	LVTTTL-I	TX DIS	Transmitter Disable; Turns off transmitter laser output	
6		VCC5	+5V Power Supply (Not required)	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTTL-I/O	SCL	2-Wire Serial Interface Clock	2
11	LVTTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
12	LVTTTL-O	MOD Ab	Indicates Module is not present. Grounded in the Module	2
13	LVTTTL-O	MOD NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTTL-O	RX LOS	Receiver Loss Of Signal Indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RDN	Receiver Inverted Data Output	
18	CML-O	RDP	Receiver Non-Inverted Data Output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply (Not required).	3
21	LVTTTL-I	P_DOWN/RST	Powerdown; When high, requires the module to limit power consumption to 1.5W or below. 2-Wire serial interface must be functional in the low power mode.	
21	LVTTTL-I	P_DOWN/RST	Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power	
22		VCC2	+1.8V Power Supply (Not required)	3
23		GND	Module Ground	1
24	PECL-I	REFCLK	Not used, internally terminated to 50ohm (100ohm diff).	4
25	PECL-I	REFCLK	Not used, internally terminated to 50ohm (100ohm diff).	4
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TDN	Transmitter Inverted Data Input	
29	CML-I	TDP	Transmitter Non-Inverted Data Input	
30		GND	Module Ground	1

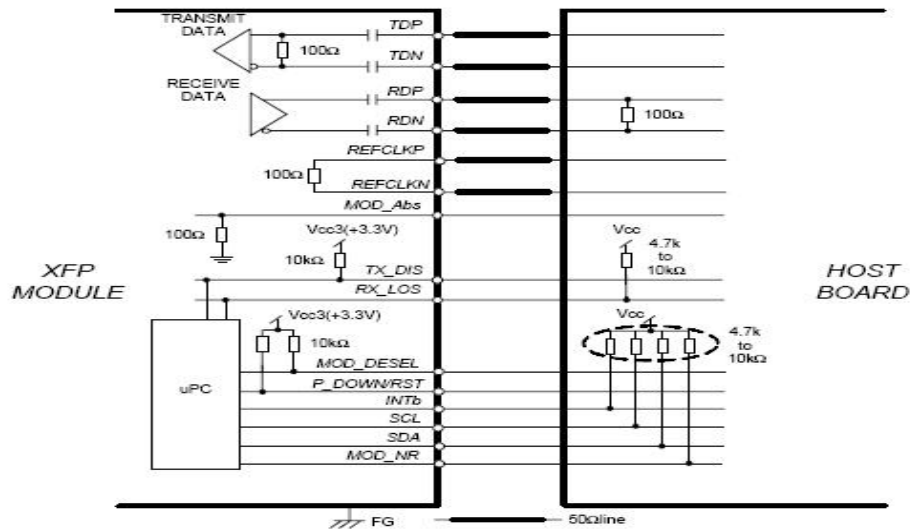
Note: 1. Module ground pins GND are isolated from the module case and chassis ground within the module.

2. Open collector; Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.6V on the host board.

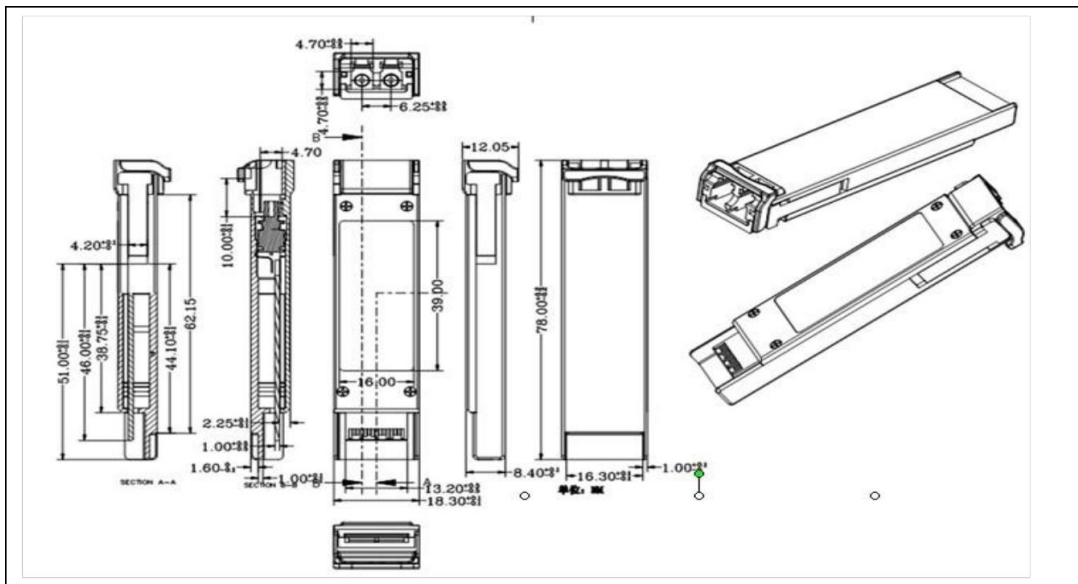
3. The pins are open within module.

4. Reference Clock is not required.

TYPICAL INTERFACE CIRCUIT



PACKAGE DIMENSIONS



ORDERING INFORMATION

PartNumber	Description
TOP-XFP-DWDM-ER-XX	XFP , 10.3125Gbps, DWDM, 40KM, 0~70°C , withDDM
TOP-XFP-DWDM-ZR-XX	XFP , 10.3125Gbps, DWDM, 80KM, 0~70°C , withDDM
TOP-XFP-CWDM-ER-XX	XFP , 10.3125Gbps, CWDM, 40KM, 0~70°C , withDDM
TOP-XFP-CWDM-ZR-XX	XFP , 10.3125Gbps, CWDM, 80KM, 0~70°C , withDDM



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